

SHEET

TITLE

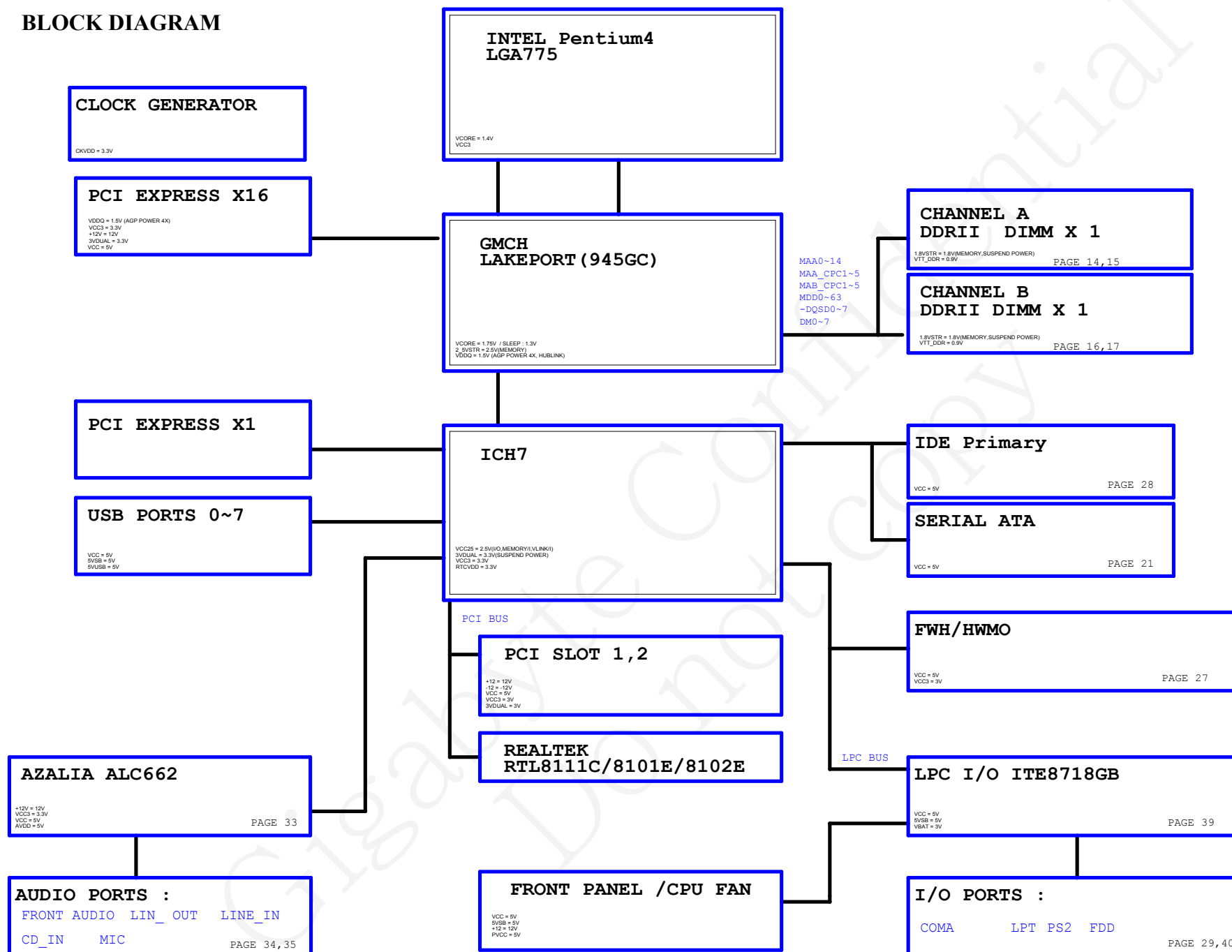
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B
06	P4 LGA775 C
07	P4 LGA775 D,E,F,G
08	GMCH-LAKEPORT HOST
09	GMCH-LAKEPORT DDRII
10	GMCH-LAKEPORT PCI E, DMI
11	GMCH-LAKEPORT INT VGA
12	GMCH-LAKEPORT GND
13	GMCH-LAKEPORT PWR
14	DDRII CHANNEL A 1
15	DDRII CHANNEL B 2
16	DDRII TERMINATION
17	PCI EXPRESS*16 SLOT
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	GB/CK505-OC CLOCK.
22	PCI SLOT 1,2,PCIEX1
23	IDE/FLOPPY
24	ITE 8718 GBIX
25	COM LPT
26	BIOS,CI,HWM,KB/MS
27	AZALIA ALC662

SHEET

TITLE

28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM ISL6312
31	ATX, OTHERS POWER
32	REALTEK RTL8111C/8101E/8102E
33	FRONT PANEL

# BLOCK DIAGRAM



Model Name : 945GCM-S2L

**Version: 1.02**

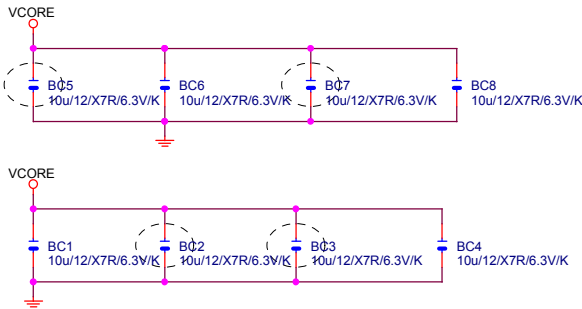
## Component value change history

2007/09/27

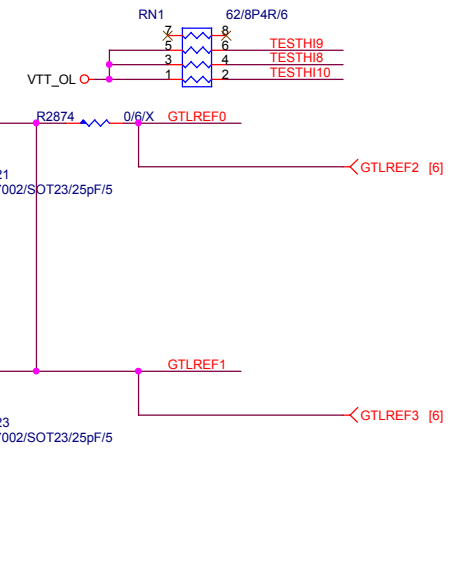
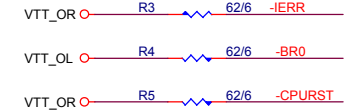
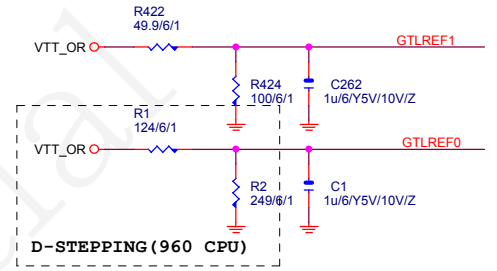
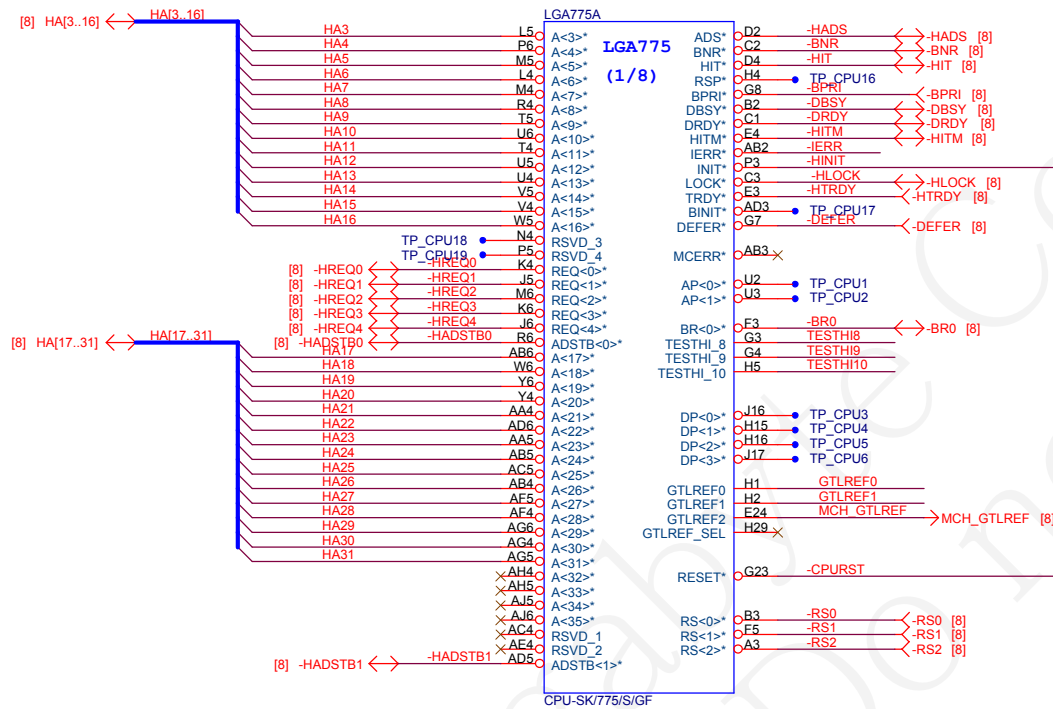
[illegible]

Circuit or PCB layout change  
for next version

[illegible]



footprint: LGA775-3A9



Gigabyte Technology			
Title			
P4_LGA775-A			
Size	Document Number	945GCM-S2L	
		Rev	1.02
Date:	Tuesday, October 02, 2007	Sheet	4 of 33

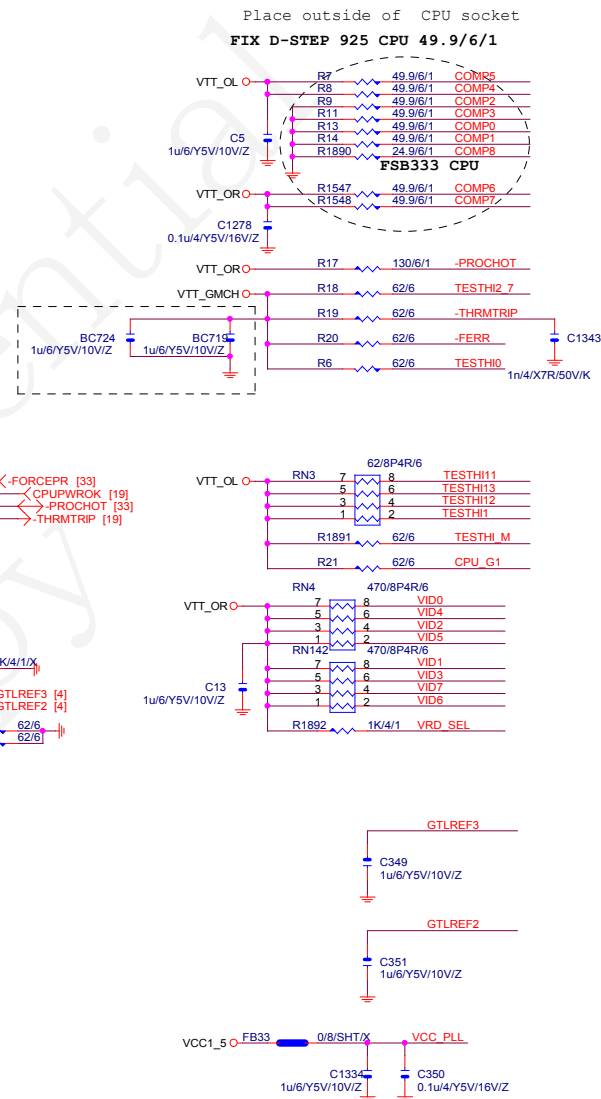


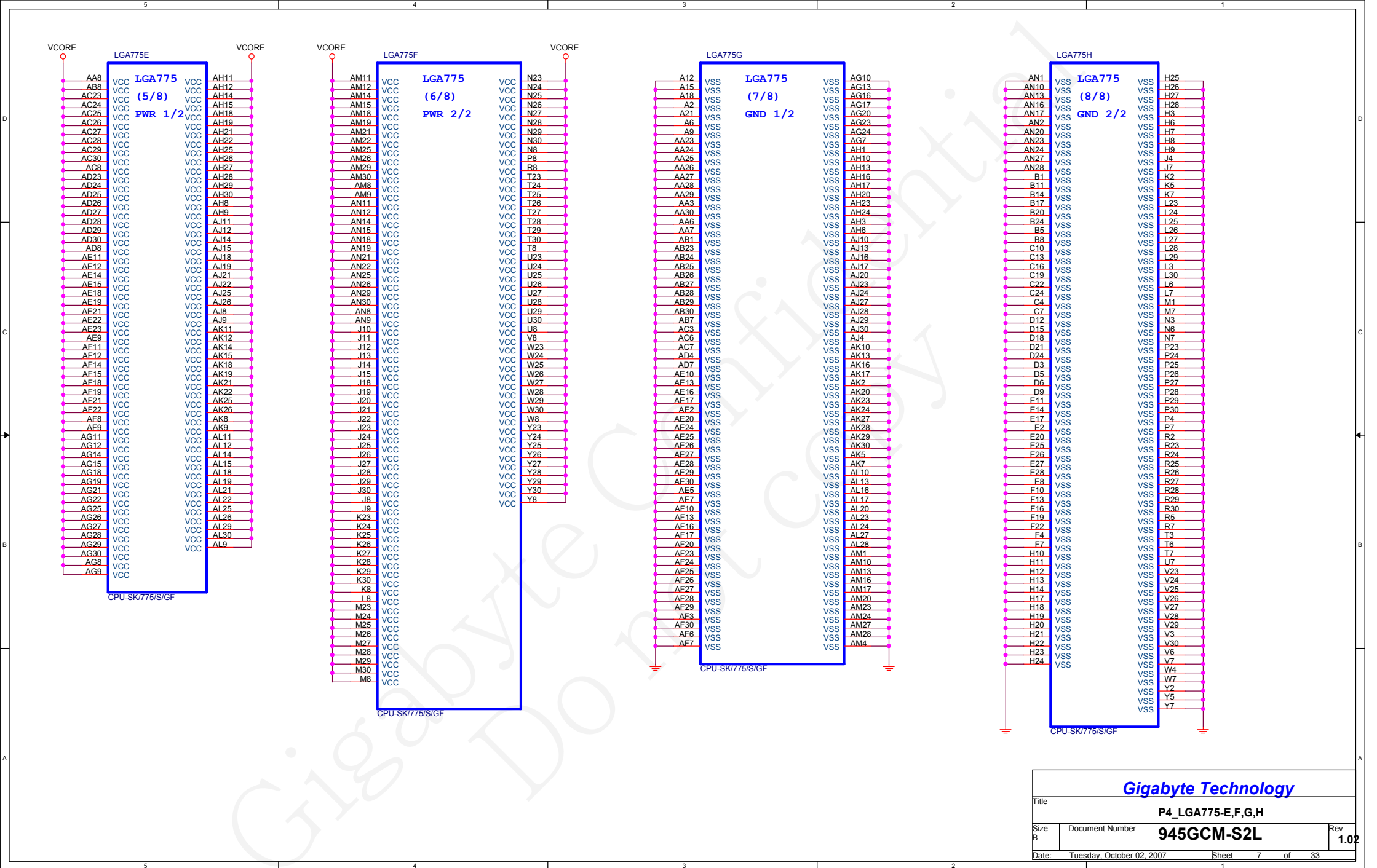
[illegible]

As close as possible to  
CPU socket

FSA	FSB	NA	
FBSSEL0	FBSSEL1	FBSSEL3	Clock
1	0	1	100MHz
1	0	0	133MHz
1	1	0	166MHz
0	1	0	200MHz
0	0	0	266MHz

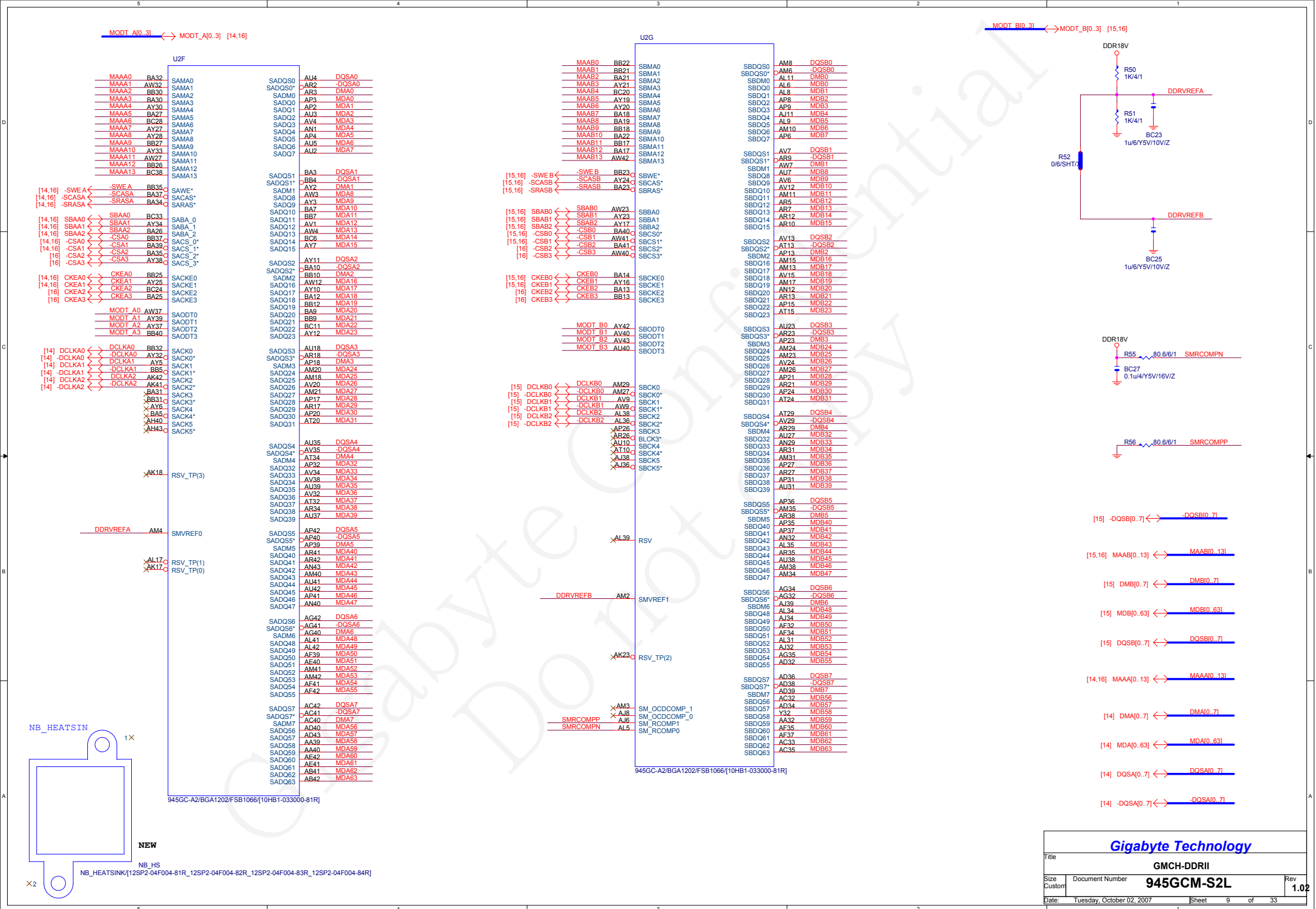
10LI2-12100A-13=INDUCTOR 10uH 300mA TAI-TECH  
10LI2-12100A-02=INDUCTOR 10uH 155mA TAIYO  
10LI2-12100A-01=INDUCTOR 10uH 120mA TDK

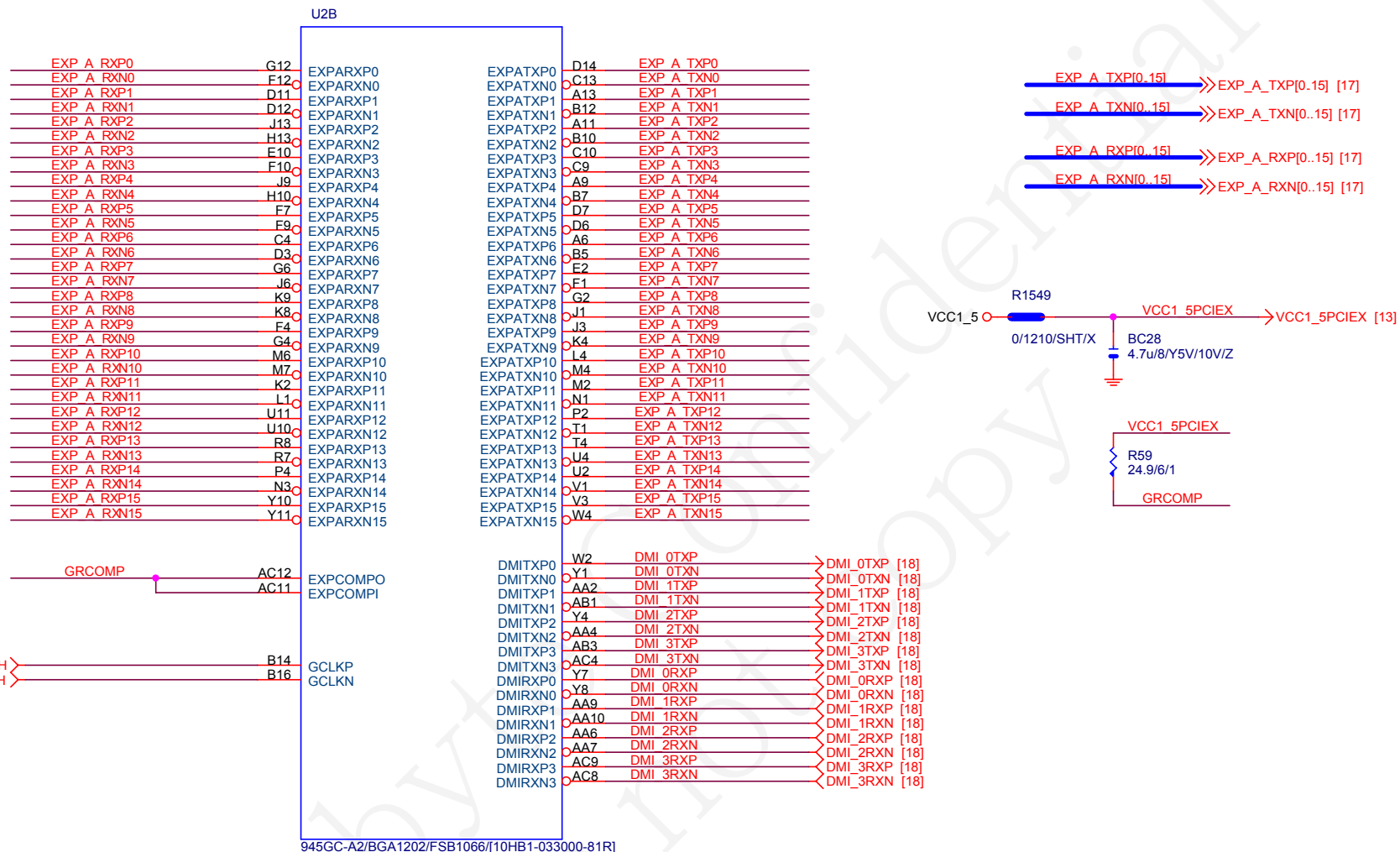














U2C

A4	VSS	N2
A16	VSS	N6
A22	VSS	N8
A26	VSS	N13
A31	VSS	N15
A35	VSS	N23
B4	VSS	N26
B6	VSS	N27
B9	VSS	N29
B11	VSS	N31
B13	VSS	N33
B21	VSS	N36
B22	VSS	N39
B28	VSS	N43
B33	VSS	P3
B38	VSS	P14
C3	VSS	P15
C5	VSS	P24
C7	VSS	P26
C12	VSS	P27
C14	VSS	P29
C22	VSS	P30
C40	VSS	R6
D2	VSS	R9
D5	VSS	R12
D10	VSS	R14
D16	VSS	R30
D20	VSS	R31
D21	VSS	R34
E3	VSS	R37
E4	VSS	R39
E7	VSS	T2
E9	VSS	T42
E12	VSS	U3
E13	VSS	U5
E17	VSS	U12
E18	VSS	U14
E20	VSS	U31
E21	VSS	U33
E32	VSS	U36
F2	VSS	U38
F6	VSS	V2
F13	VSS	V8
F18	VSS	V11
F26	VSS	V12
F34	VSS	V14
F42	VSS	V14
G3	VSS	V36
G5	VSS	V37
G7	VSS	V38
G9	VSS	V39
G10	VSS	V43
G13	VSS	W3
G15	VSS	Y2
G18	VSS	Y5
G20	VSS	Y6
G21	VSS	Y9
G24	VSS	Y12
G27	VSS	Y14
G29	VSS	Y31
G31	VSS	Y35
G32	VSS	Y37
G35	VSS	Y39
G38	VSS	Y42
H12	VSS	AA3
H17	VSS	AA8
H26	VSS	AA11
H27	VSS	AA12
H32	VSS	AA14
J2	VSS	AA21
J5	VSS	AA23
J7	VSS	AA31
J10	VSS	AA33
J12	VSS	AA36
J21	VSS	AB2
J24	VSS	AB43
J29	VSS	AC2
J38	VSS	AC3
J43	VSS	AC7
K3	VSS	AC10
K5	VSS	AC14
K6	VSS	AC21
K7	VSS	AC23
K10	VSS	AC31
K12	VSS	AC36
K13	VSS	AC37
K15	VSS	AC39
K20	VSS	AC39
K27	VSS	
K32	VSS	
K34	VSS	
K37	VSS	
K39	VSS	
L2	VSS	
L12	VSS	
L13	VSS	
L24	VSS	
L26	VSS	
L29	VSS	
L31	VSS	
L42	VSS	
M3	VSS	
M5	VSS	
M8	VSS	
M9	VSS	
M10	VSS	
M13	VSS	
M20	VSS	
M21	VSS	
M35	VSS	
M37	VSS	

945GC-A2/BGA1202/FSB1066/[10HB1-033000-81R]

U2E

AD7	VSS
AD9	VSS
AD11	VSS
AD13	VSS
AD33	VSS
N24	VSS
AD37	VSS
AD42	VSS
AF1	VSS
AF2	VSS
N33	VSS
AF5	VSS
N39	VSS
AF36	VSS
P3	VSS
P14	VSS
AG30	VSS
AG31	VSS
AG33	VSS
AG36	VSS
P29	VSS
AG38	VSS
AG39	VSS
AH42	VSS
R12	VSS
A10	VSS
A30	VSS
A31	VSS
R34	VSS
A35	VSS
A37	VSS
AK24	VSS
AK26	VSS
AK29	VSS
AK30	VSS
U9	VSS
AL1	VSS
AL2	VSS
AL3	VSS
AL7	VSS
U33	VSS
AL10	VSS
AL12	VSS
AL13	VSS
AL15	VSS
AL18	VSS
AL21	VSS
AL23	VSS
AL24	VSS
AL27	VSS
AL32	VSS
AL33	VSS
AL37	VSS
V38	VSS
AM5	VSS
AM7	VSS
Y2	VSS
AM33	VSS
Y6	VSS
AM36	VSS
AM37	VSS
AM39	VSS
AN2	VSS
AN4	VSS
AN13	VSS
Y37	VSS
AN17	VSS
AN18	VSS
AN20	VSS
AN21	VSS
AN23	VSS
AN24	VSS
AN26	VSS
AN27	VSS
AN31	VSS
AN42	VSS
AP5	VSS
AP7	VSS
AP10	VSS
AP12	VSS
AP20	VSS
AP34	VSS
AP38	VSS
AR1	VSS
AR6	VSS
AR15	VSS
AR20	VSS
AR24	VSS
AR32	VSS
AR37	VSS
AR39	VSS
AR43	VSS
AT12	VSS
AT17	VSS
AT18	VSS
AT21	VSS
AT23	VSS
AT26	VSS
AT27	VSS
AT31	VSS
AU6	VSS
AU9	VSS
AU12	VSS
AU13	VSS
AU15	VSS
AU17	VSS
AU20	VSS
AD18	VSS
AD20	VSS
AD22	VSS
AD24	VSS
AD27	VSS
AD29	VSS
AE19	VSS
AE21	VSS
AE23	VSS
AE25	VSS
AF18	VSS
AF20	VSS
AF22	VSS
AF24	VSS
AY1	VSS
BC4	VSS

945GC-A2/BGA1202/FSB1066/[10HB1-033000-81R]

VSS	AU21
VSS	AU24
VSS	AU26
VSS	AU29
VSS	AU32
VSS	AU34
VSS	AV2
VSS	AV10
VSS	AV17
VSS	AV37
VSS	AW10
VSS	BA4
VSS	BA42
VSS	BB3
VSS	BB6
VSS	BB11
VSS	BB14
VSS	BB19
VSS	BB34
VSS	BB39
VSS	BB41
VSS	BC9
VSS	A40
VSS	D1
VSS	D43
VSS	R26
VSS	R29
VSS	U29
VSS	V24
VSS	V26
VSS	V29
VSS	W21
VSS	W23
VSS	W25
VSS	Y20
VSS	Y22
VSS	Y24
VSS	Y26
VSS	Y29
VSS	AA25
VSS	AA27
VSS	AA29
VSS	AC19
VSS	AC25
VSS	AC29

NC	AA2
NC	B2
NC	B3
NC	B43
NC	B42
NC	B43
NC	C2
NC	C42
NC	E35
NC	AV26
NC	AV27
NC	AW2
NC	AW28
NC	BA2
NC	BB1
NC	BB2
NC	BB43
NC	BC1
NC	BC2
NC	BC42
NC	BC43

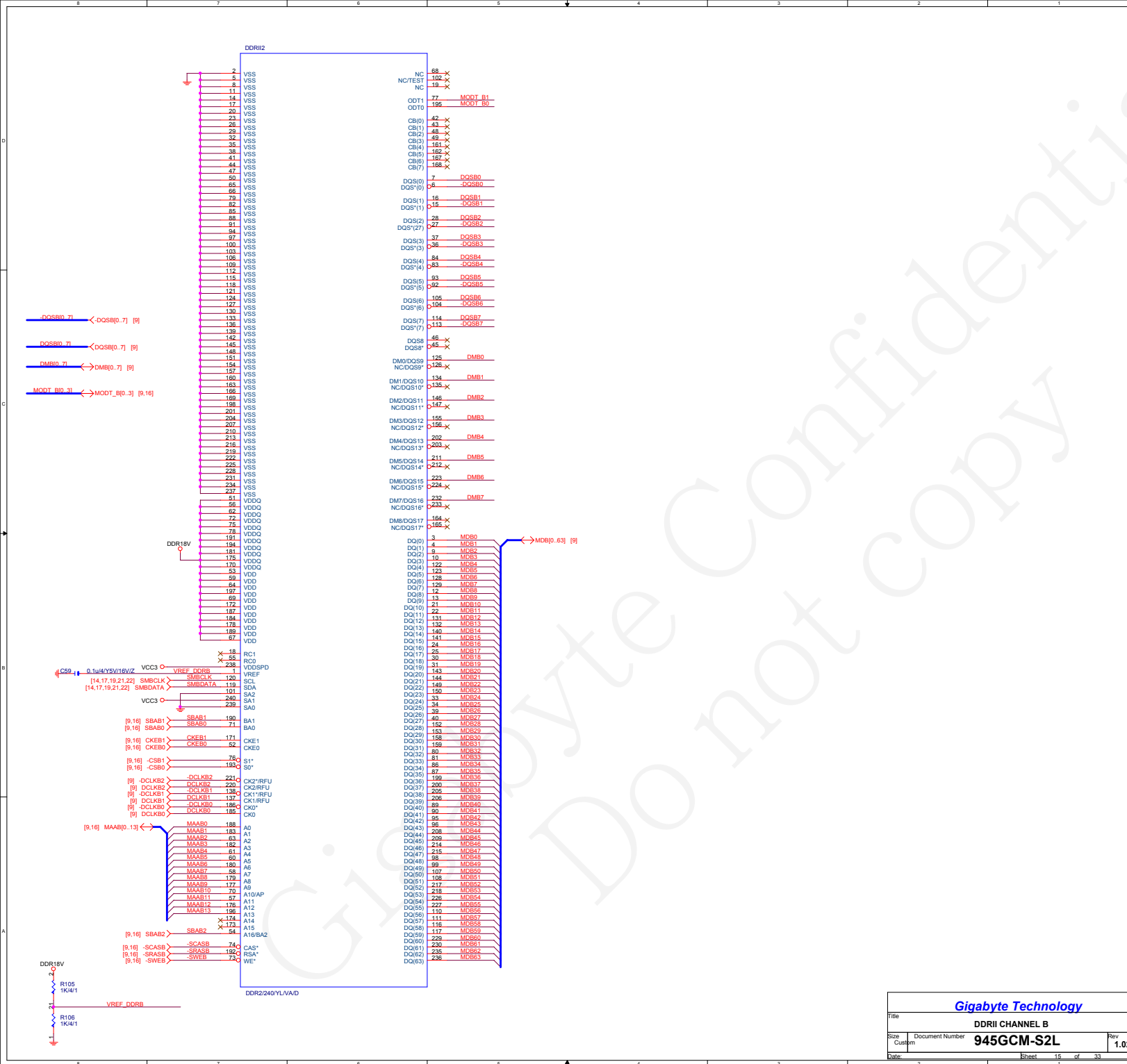
RSVD	AK21
RSVD	AJ23
RSVD	AJ26
RSVD	AL29
RSVD	AL50
RSVD	AJ21
RSVD	AL26
RSVD	AK27
RSVD	AD36
RSVD	AC34
RSVD	Y30
RSVD	Y33
RSVD	AF31
RSVD	AD31
RSVD	U31
RSVD	V31
RSVD	AA30
RSVD	AC30
RSVD	AA3
RSVD	AG25
RSVD	AG28
RSVD	AG27
RSVD	AJ29
RSVD	AJ27
RSVD	AK40
RSVD	AW17
RSVD	AW18
RSVD	AY14
RSVD	BC16
RSVD	AJ25
RSVD	AG29

Gigabyte Technology

Title			GMCH-GND	
Size			Document Number	
Custom			945GCM-S2L	
Date			Tuesday, October 02, 2007	
			Sheet 12 of 33	
			Rev 1.02	



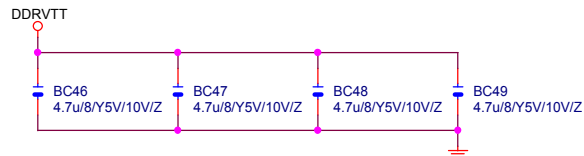
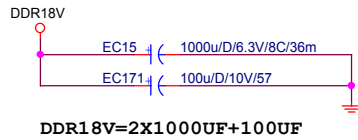




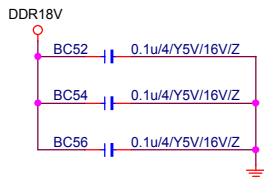


# DDR TERMINATION CHANNEL A

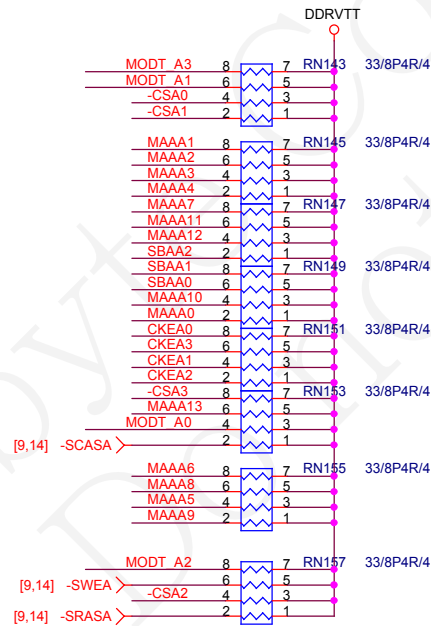
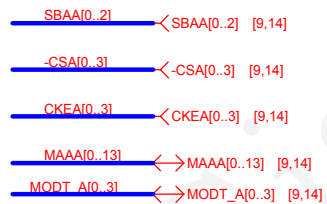
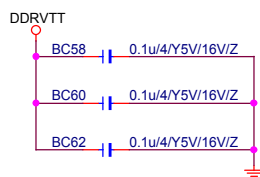
## DDRVTT Decouple



## DDR18V Decouple

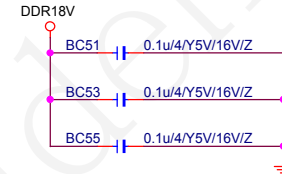


## DDRVTT Decouple

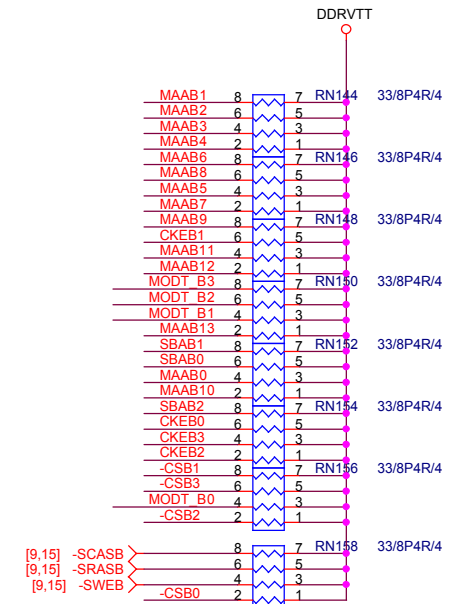
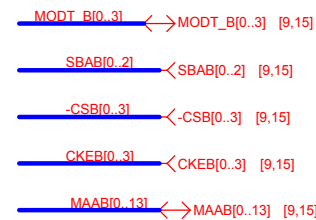
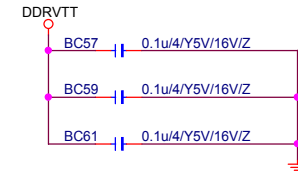


# DDR TERMINATION CHANNEL B

## DDR18V Decouple



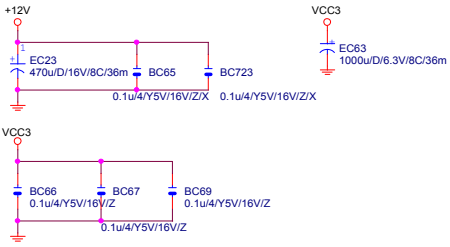
## DDRVTT Decouple



Gigabyte Technology

Title			
DDRII TERMINATOR			
Size	Document Number	945GCM-S2L	
Custom			Rev 1.02
Date:	Tuesday, October 02, 2007	Sheet 16	of 33

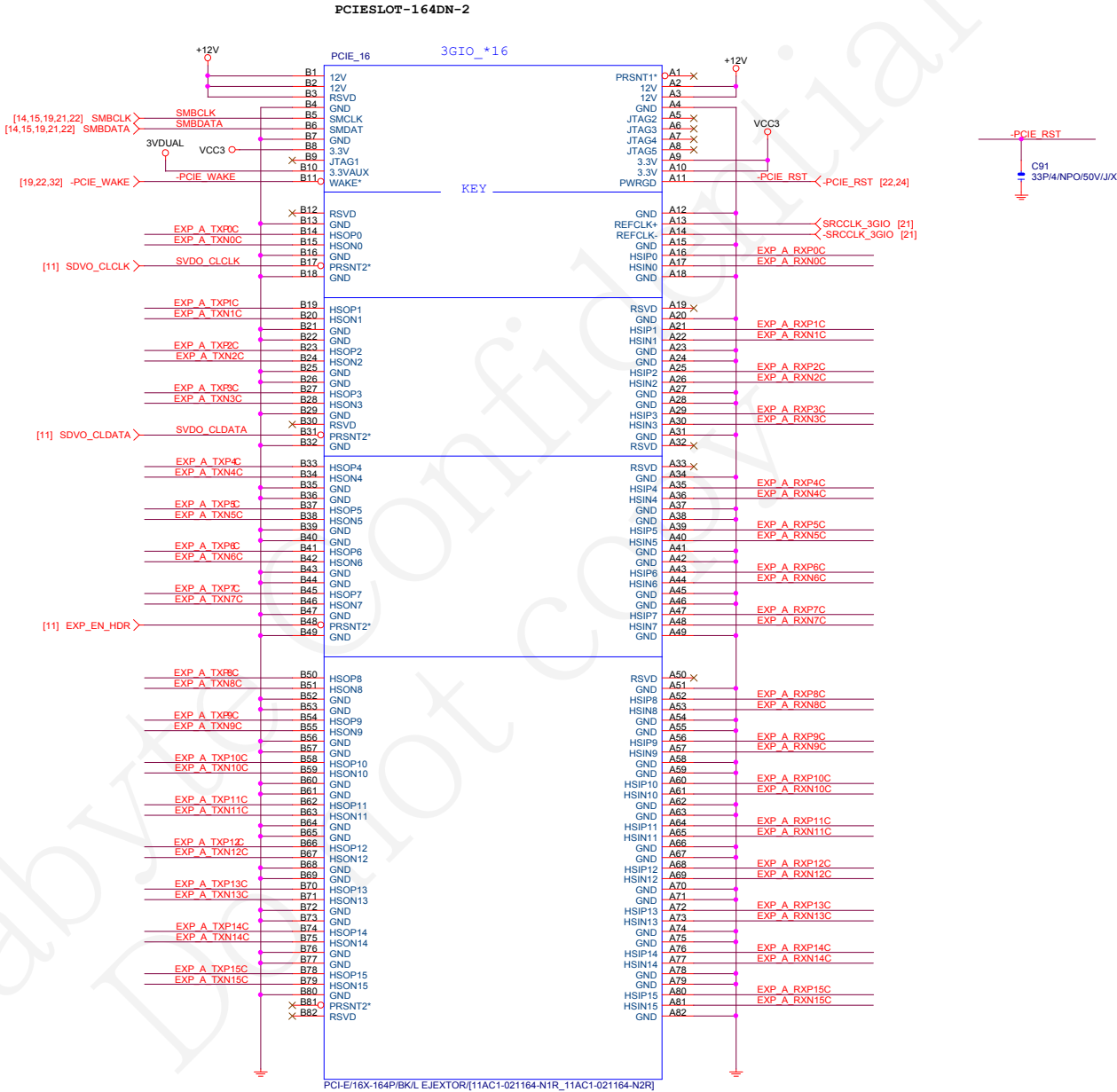




EXP A TXP0\_15] >> EXP\_A\_TXP0\_15] [10] EXP A RXP0\_15] >> EXP\_A\_RXP0\_15] [10]  
EXP A TXN0\_15] >> EXP\_A\_TXN0\_15] [10] EXP A RXN0\_15] >> EXP\_A\_RXN0\_15] [10]

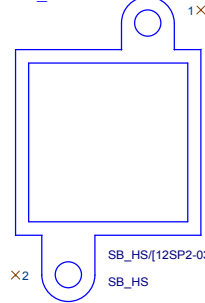
EXP A TXP0	C92	0.1u4Y5V/16V/Z	EXP A TXP0C
EXP A TXP1	C93	0.1u4Y5V/16V/Z	EXP A TXP0C
EXP A TXP1	C94	0.1u4Y5V/16V/Z	EXP A TXP1C
EXP A TXN1	C95	0.1u4Y5V/16V/Z	EXP A TXN1C
EXP A TXP2	C96	0.1u4Y5V/16V/Z	EXP A TXP2C
EXP A TXN2	C97	0.1u4Y5V/16V/Z	EXP A TXN2C
EXP A TXP3	C98	0.1u4Y5V/16V/Z	EXP A TXP3C
EXP A TXN3	C99	0.1u4Y5V/16V/Z	EXP A TXN3C
EXP A TXP4	C100	0.1u4Y5V/16V/Z	EXP A TXP4C
EXP A TXN4	C101	0.1u4Y5V/16V/Z	EXP A TXN4C
EXP A TXP5	C102	0.1u4Y5V/16V/Z	EXP A TXP5C
EXP A TXN5	C103	0.1u4Y5V/16V/Z	EXP A TXN5C
EXP A TXP6	C104	0.1u4Y5V/16V/Z	EXP A TXP6C
EXP A TXN6	C105	0.1u4Y5V/16V/Z	EXP A TXN6C
EXP A TXP7	C106	0.1u4Y5V/16V/Z	EXP A TXP7C
EXP A TXN7	C107	0.1u4Y5V/16V/Z	EXP A TXN7C
EXP A TXP8	C108	0.1u4Y5V/16V/Z	EXP A TXP8C
EXP A TXN8	C109	0.1u4Y5V/16V/Z	EXP A TXN8C
EXP A TXP9	C110	0.1u4Y5V/16V/Z	EXP A TXP9C
EXP A TXN9	C111	0.1u4Y5V/16V/Z	EXP A TXN9C
EXP A TXP10	C112	0.1u4Y5V/16V/Z	EXP A TXP10C
EXP A TXN10	C113	0.1u4Y5V/16V/Z	EXP A TXN10C
EXP A TXP11	C114	0.1u4Y5V/16V/Z	EXP A TXP11C
EXP A TXN11	C115	0.1u4Y5V/16V/Z	EXP A TXN11C
EXP A TXP12	C116	0.1u4Y5V/16V/Z	EXP A TXP12C
EXP A TXN12	C117	0.1u4Y5V/16V/Z	EXP A TXN12C
EXP A TXP13	C118	0.1u4Y5V/16V/Z	EXP A TXP13C
EXP A TXN13	C119	0.1u4Y5V/16V/Z	EXP A TXN13C
EXP A TXP14	C120	0.1u4Y5V/16V/Z	EXP A TXP14C
EXP A TXN14	C121	0.1u4Y5V/16V/Z	EXP A TXN14C
EXP A TXP15	C122	0.1u4Y5V/16V/Z	EXP A TXP15C
EXP A TXN15	C123	0.1u4Y5V/16V/Z	EXP A TXN15C

EXP A RXP0	EXP A RXN0C
EXP A RXN0	EXP A RXN0C
EXP A RXP1	EXP A RXN1C
EXP A RXN1	EXP A RXN1C
EXP A RXP2	EXP A RXN2C
EXP A RXN2	EXP A RXN2C
EXP A RXP3	EXP A RXN3C
EXP A RXN3	EXP A RXN3C
EXP A RXP4	EXP A RXN4C
EXP A RXN4	EXP A RXN4C
EXP A RXP5	EXP A RXN5C
EXP A RXN5	EXP A RXN5C
EXP A RXP6	EXP A RXN6C
EXP A RXN6	EXP A RXN6C
EXP A RXP7	EXP A RXN7C
EXP A RXN7	EXP A RXN7C
EXP A RXP8	EXP A RXN8C
EXP A RXN8	EXP A RXN8C
EXP A RXP9	EXP A RXN9C
EXP A RXN9	EXP A RXN9C
EXP A RXP10	EXP A RXN10C
EXP A RXN10	EXP A RXN10C
EXP A RXP11	EXP A RXN11C
EXP A RXN11	EXP A RXN11C
EXP A RXP12	EXP A RXN12C
EXP A RXN12	EXP A RXN12C
EXP A RXP13	EXP A RXN13C
EXP A RXN13	EXP A RXN13C
EXP A RXP14	EXP A RXN14C
EXP A RXN14	EXP A RXN14C
EXP A RXP15	EXP A RXN15C
EXP A RXN15	EXP A RXN15C

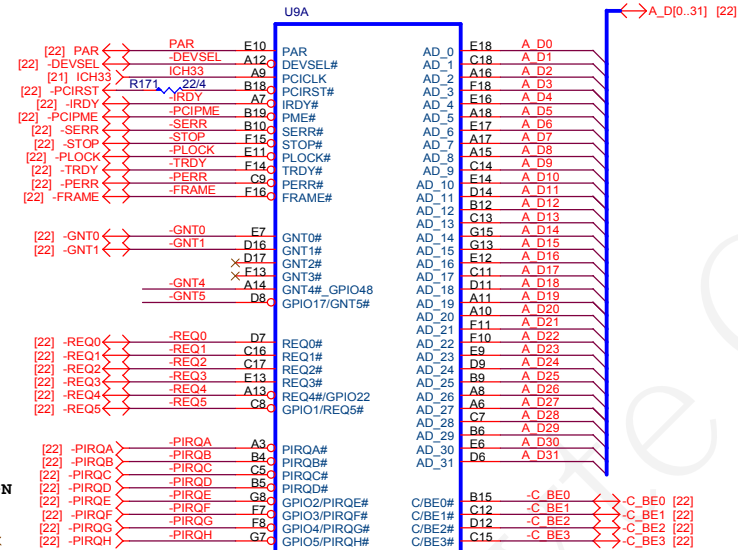


NEW  
SB\_HS:BGHSAINK\_SB-N

SB\_HEATSIN



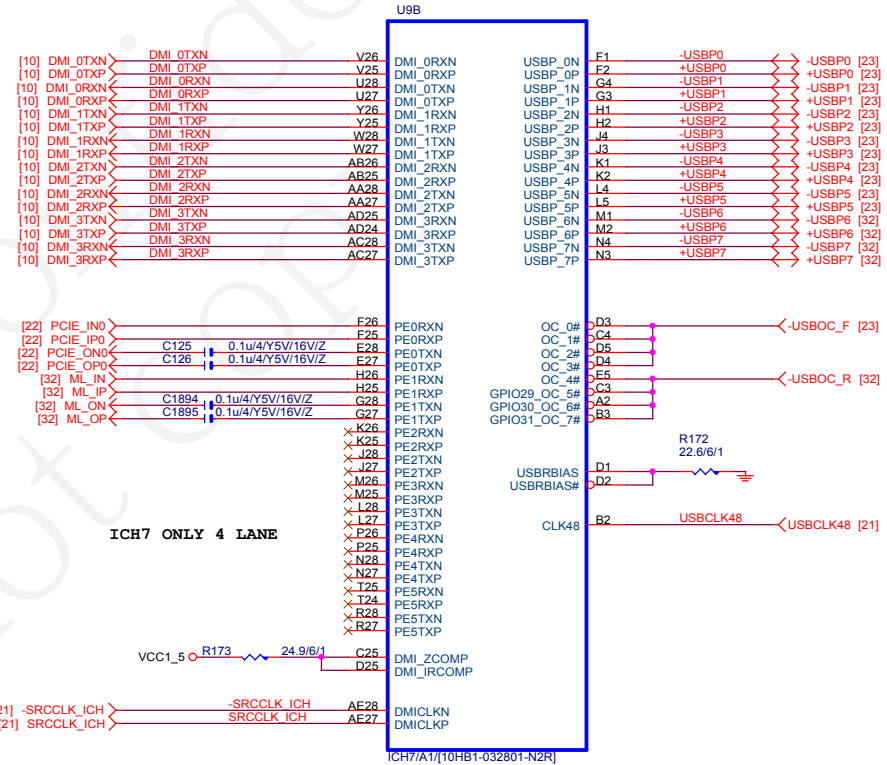
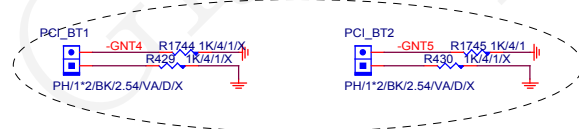
NEW SB\_HS 對邊



ICH7/A1/[10HB1-032801-N2R]

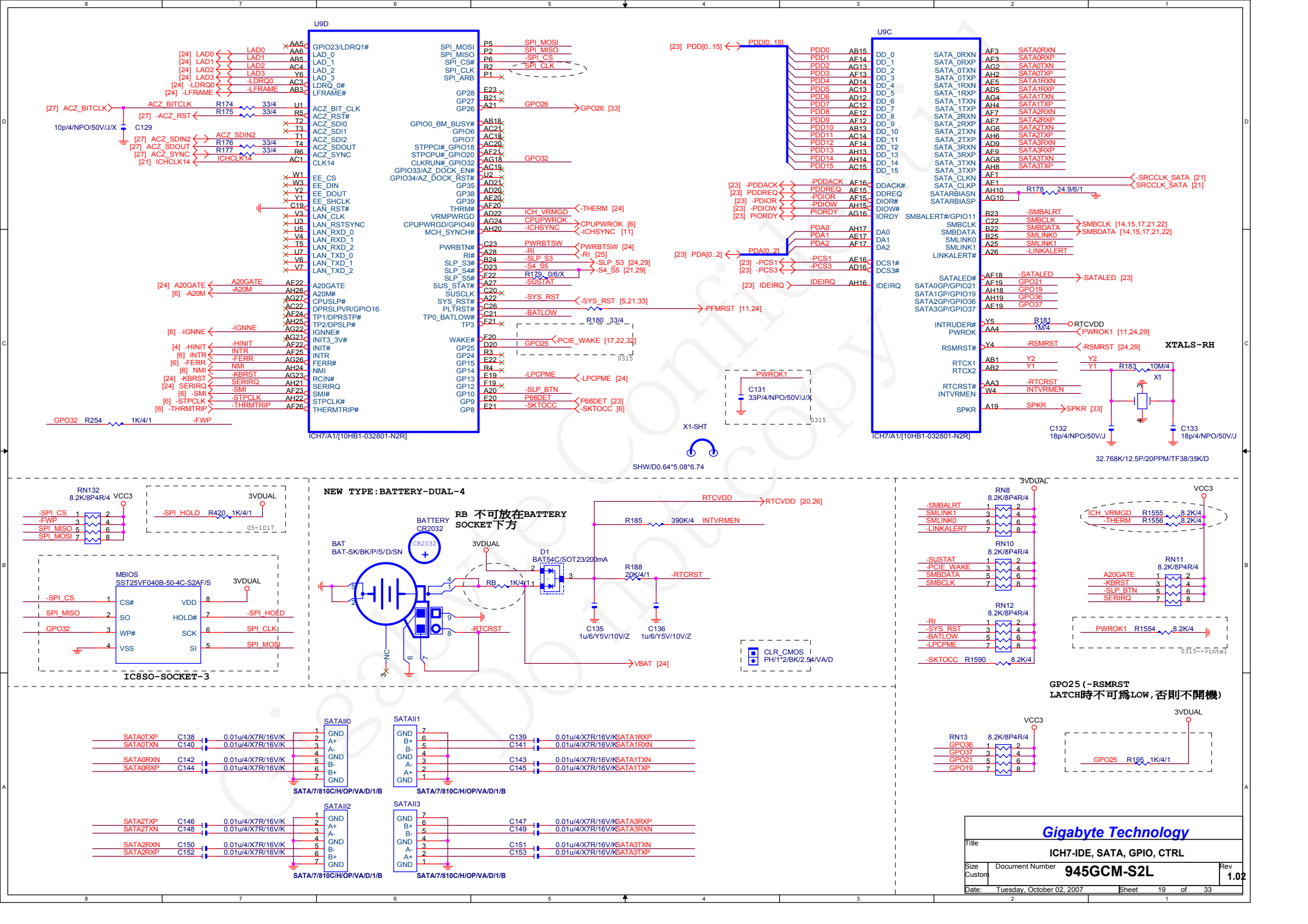
01=SPI  
10=PCI  
11=LPC (DEFAULT)

Pb-Free

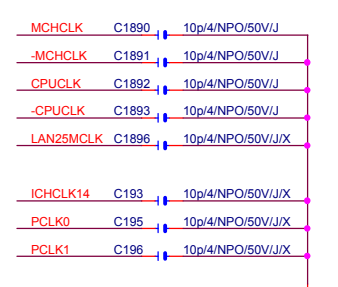


Gigabyte Technology

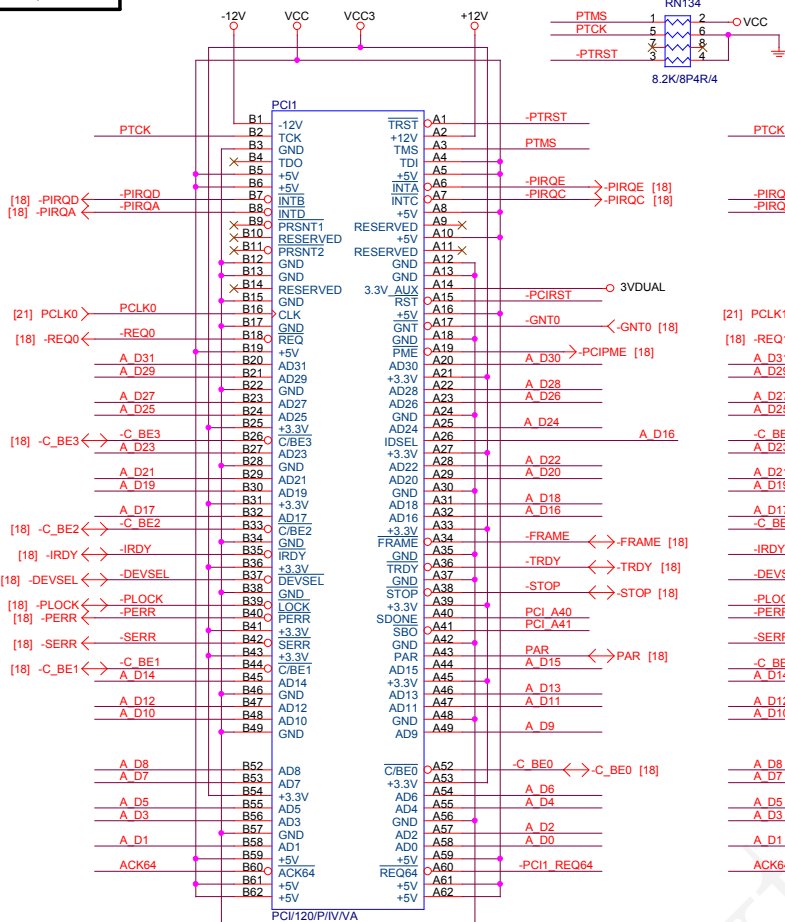
Title		
ICH7-PCI, DMI, LAN, USB		
Size	Document Number	Rev
B	945GCM-S2L	1.02
Date:	Tuesday, October 02, 2007	Sheet 18 of 33



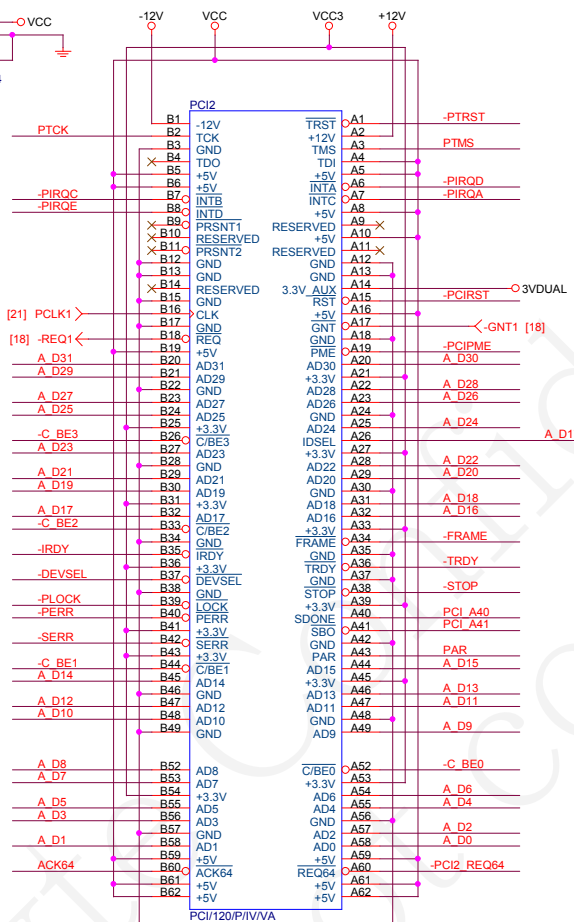




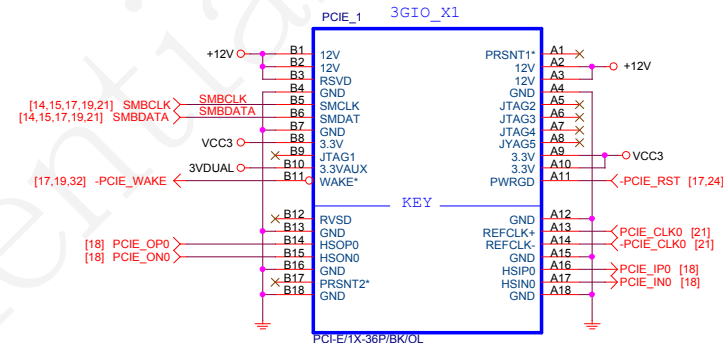
# PCI1,2 SLOT



AD\_16/-PIRQ (E-D-C-A) /-REQ0/-GNT0



AD\_17/-PIRQ (D-C-A-E) /-REQ1/-GNT1



PCIE1X-36P/BK/OL

[18] A\_D[0..31] <-> A\_D0..31

-PCIRST <-> PCIRST [18]

Place close to PCI1

[18] -REQ4 <-> REQ4  
[18] -REQ3 <-> REQ3  
[18] -REQ1 <-> REQ1  
[18] -REQ2 <-> REQ2

[18] -REQ0 <-> REQ0  
[18] PAR <-> PAR  
[18] -REQ5 <-> REQ5

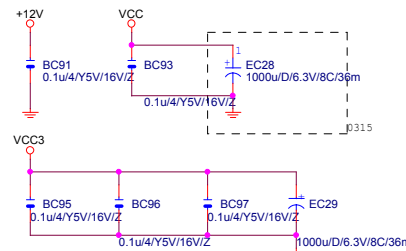
PC12 REQ64  
PC11 REQ64

-DEVSEL <-> DEVSEL  
-TRDY <-> TRDY  
-IRDY <-> IRDY  
-FRAME <-> FRAME

-SERR <-> SERR  
-PERR <-> PERR  
-PLOCK <-> PLOCK  
-STOP <-> STOP

[18] -PIRQA <-> PIRQA  
[18] -PIROD <-> PIROD  
[18] -PIROE <-> PIROE  
[18] -PIROF <-> PIROF  
[18] -PIROG <-> PIROG

[18] -PIROE <-> PIROE  
[18] -PIROF <-> PIROF  
[18] -PIROG <-> PIROG



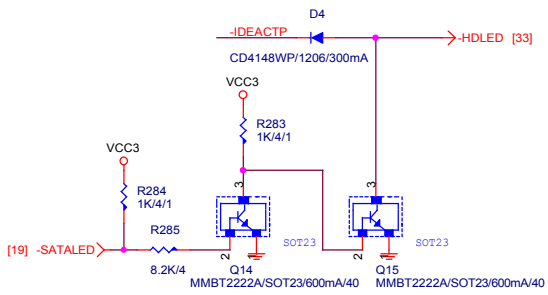
Gigabyte Technology

PCI SLOT 1, 2/PCIEX1

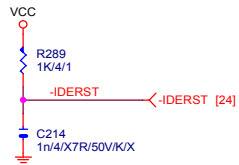
Title		945GCM-S2L	
Size	Custom	Document Number	Rev 1.02
Date: Tuesday, October 02, 2007		Sheet 22 of 33	



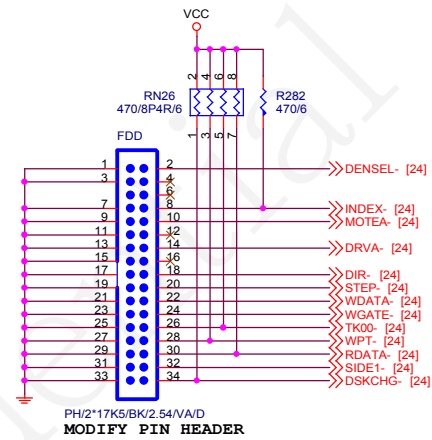
# IDE/SATA LED



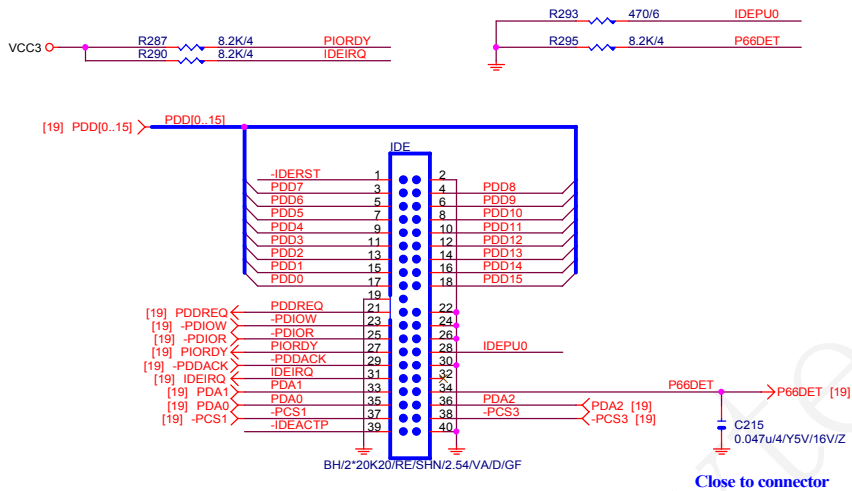
# IDE RESET



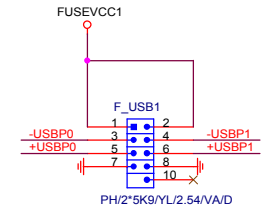
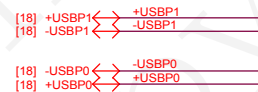
# FLOPPY



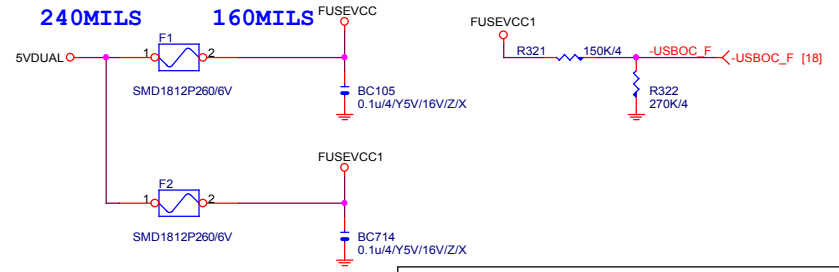
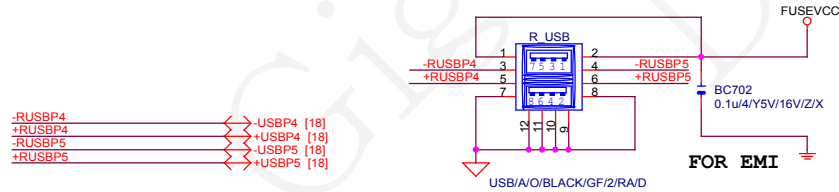
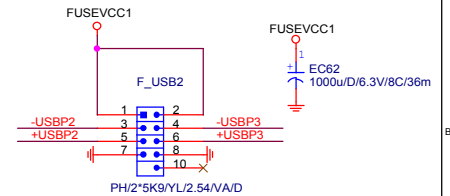
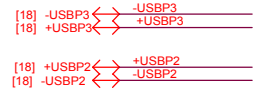
# IDE



# FRONT USB1



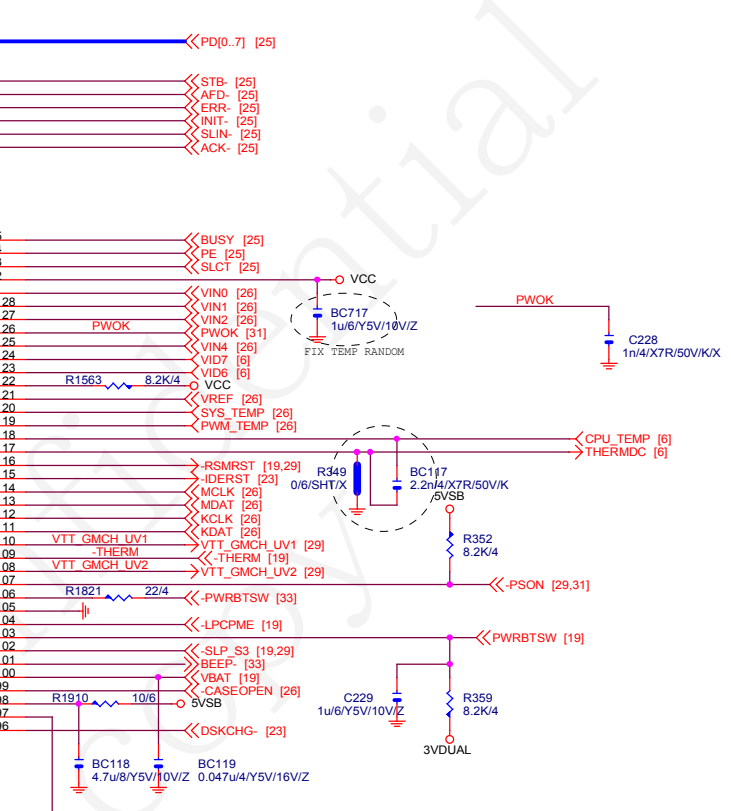
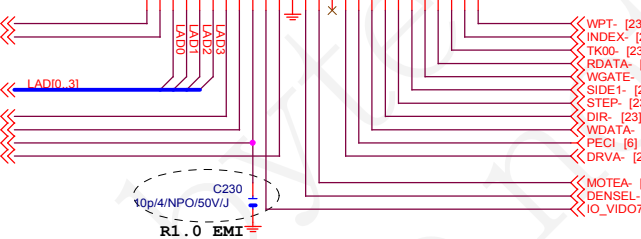
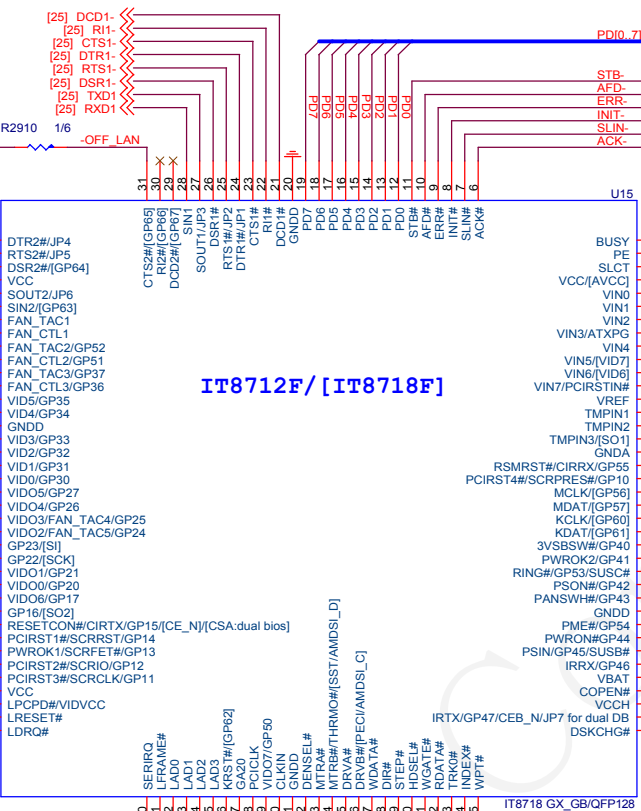
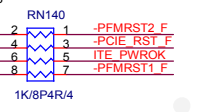
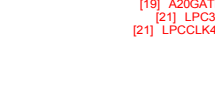
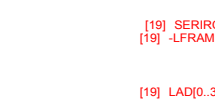
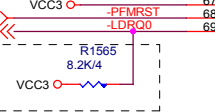
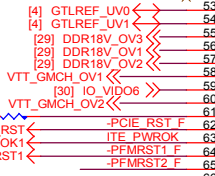
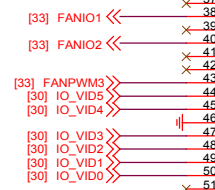
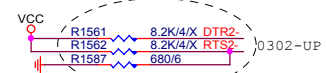
# FRONT USB2



Gigabyte Technology

Title		
IDE,FDD,F_USB		
Size B		
Document Number		
945GCM-S2L		
Date: Tuesday, October 02, 2007		
Sheet 23 of 33		
Rev 1.02		

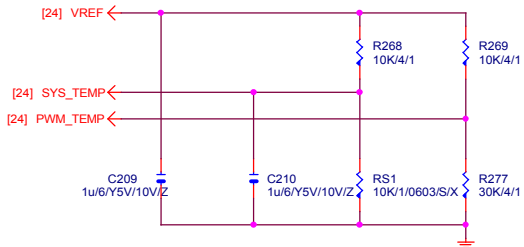
RTS2- ==LOW CPU FAN 50%  
==HIGH 100%  
**DEFAULT 50%**



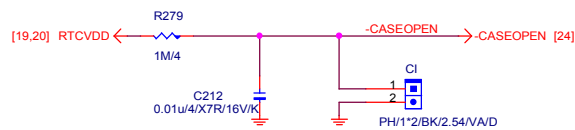




# TEMP H/W MONITOR

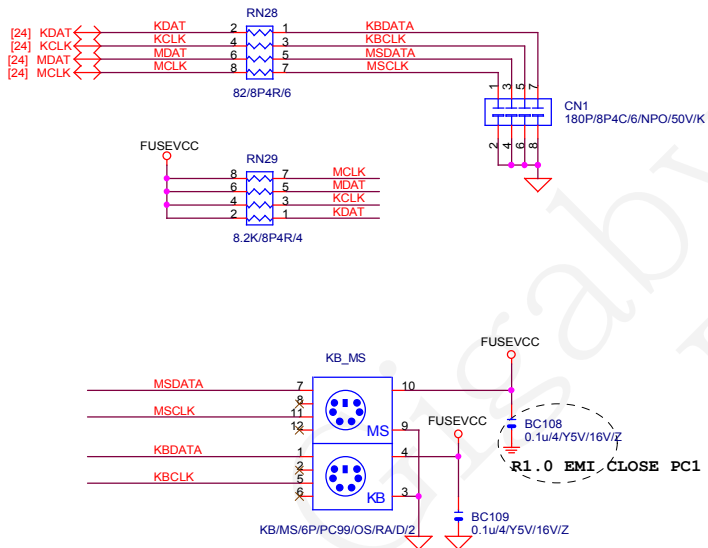


# CASE OPEN

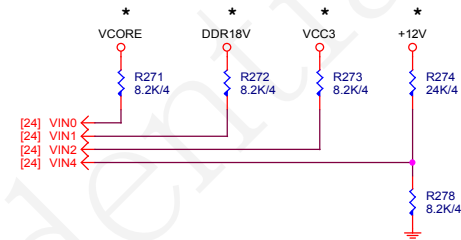


Case Open Circuits

# KB/MS

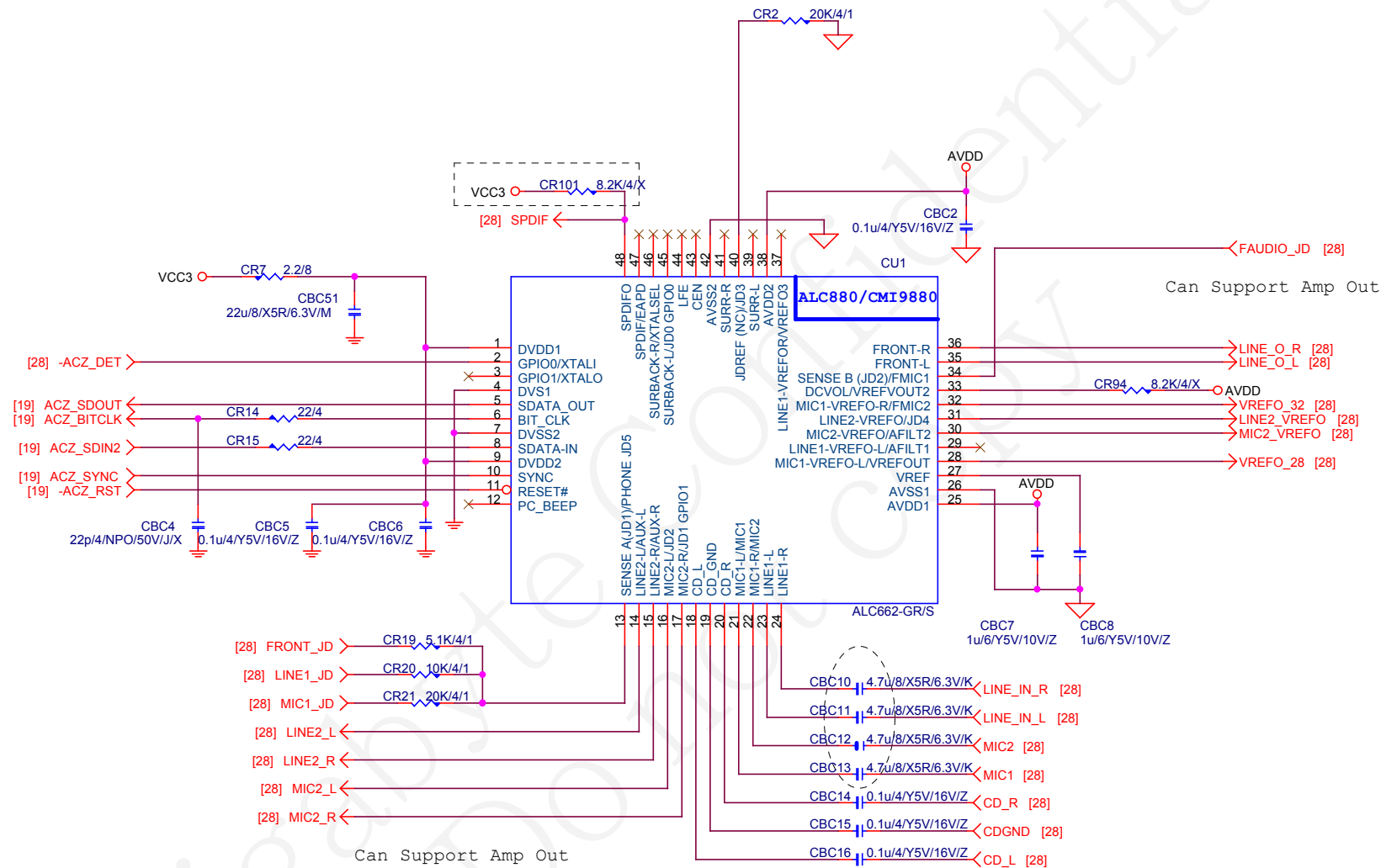


# VOLTAGE-- H/W MONITOR

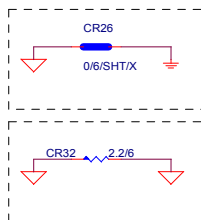


**Gigabyte Technology**

Title			BIOS/HW-MONITOR/CI/KB/MS
Size	Document Number	945GCM-S2L	
B		Rev	1.02
Date:	Tuesday, October 02, 2007	Sheet	26 of 33

**Gigabyte Technology**

Title				
AZALIA ALC662				
Size	Document Number			Rev
Custom	945GCM-S2L			1.02
Date:	Tuesday, October 02, 2007	Sheet	27	of 33



VCC

SPDIF\_O

[27] SPDIF

1

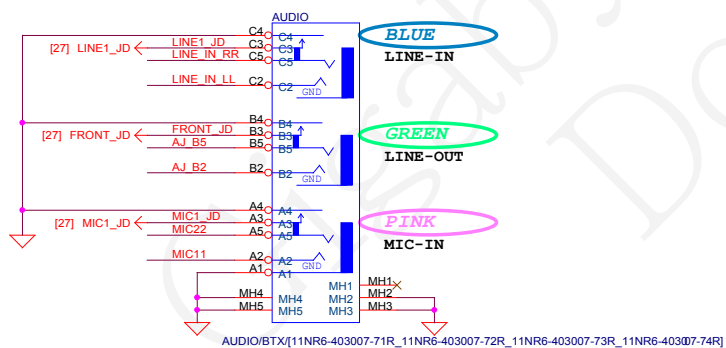
2

3

PH1/3/BK/2.54/VA/D

PIN HEADER 1X3 PIN

A diagram of a 3-port USB hub. It features three distinct ports: a USB\_1394 port (top), a USB\_LAN port (middle), and a vertical stack of three standard USB ports (bottom) colored blue, green, and red. To the left of the hub, there are two black dots representing other devices connected to the hub.



[27] LINE\_O\_R → CEC → (10uS/16V/45) → CR104 → 75/6 →

[27] LINE\_O\_L → CEC2 → (10uS/16V/45) → CR105 → 75/6 →

CR106 22K/4

CR107 100p4/NPO/50V/J

CBC21

CBC22 180p4/NPO/50V/J

AJ\_B5

AJ\_B2

[27] LINE\_IN\_R < CR108 75/6

[27] LINE\_IN\_L CR109 75/6

CR110 22K/4

CR111 22K/4

CBC26 180pF/4NPO/50V/J

CBC27 180pF/4NPO/50V/J

LINE\_IN\_RR

LINE\_IN\_LL

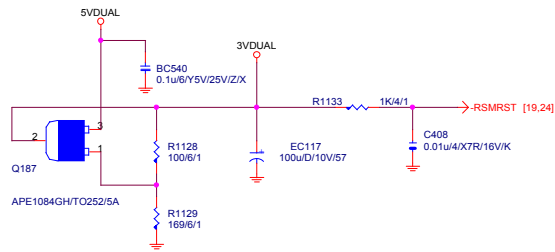
The schematic diagram illustrates the microphone input circuit. It features four input lines: [27] VREFO\_32, [27] VREFO\_28, [27] MIC2, and [27] MIC1. These lines are connected to a network of resistors and capacitors. CR112 (2.2K/4) and CR113 (2.2K/4) are in series with VREFO\_32 and VREFO\_28. CR114 (75/6) and CR115 (75/6) are in series with MIC2 and MIC1. The circuit also includes CR116 (22K/4) and CR117 (22K/4) connected to ground, and two capacitors, CBC28 and CBC29 (180pF/4/NPO/50V/J), connected to ground. The output of the circuit is labeled MIC22 and MIC11.

**AZALIA FRONT PANEL**

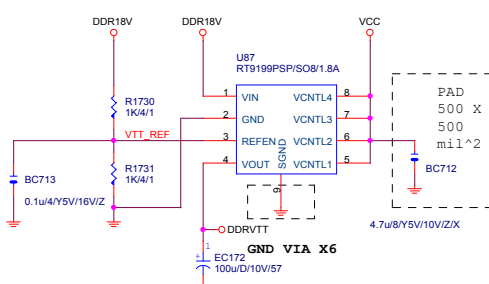
The schematic diagram illustrates the front panel circuitry, including the following components and connections:

- Power and Ground Connections:**
  - VCC3:** Connected to VCC3 and CR78 (8.2K/4).
  - PH2\*5K8/GED/2.54/V/A/D:** Connected to PH2\*5K8/GED/2.54/V/A/D.
- Resistors:**
  - CR74, CR75, CR76, CR77, CR79, CR80:** 8.2K/4.
  - CR118, CR119, CR97, CR98:** 22K/4.
  - CR120, CR121, CR99, CR100:** 75/6.
  - CR79, CR80:** 20K/4/1.
- Capacitors:**
  - CBC45, CBC44:** 4.7u/8Y5V/10V/Z.
  - CEC9:** 100u/D/10V/57.
  - CEC10:** 100u/D/10V/57.
  - CBC46, CBC47, CBC48, CBC49:** 180p/4/NPO/50V/J.
- ICs and Packages:**
  - CQ8, CQ9:** BAT54A/SOT23/200mA.
  - SOT23:** Packages for CQ8 and CQ9.
  - PH2\*5K8/GED/2.54/V/A/D:** A multi-pin connector.
- Other Labels:**
  - [27] LINE2\_VREFO, [27] MIC2\_VREFO, [27] MIC2\_L, [27] MIC2\_R, [27] LINE2\_R, [27] LINE2\_L:** Input/output signals.
  - AX\_DET [27], F\_AUDIO\_ID [27]:** Output signals.

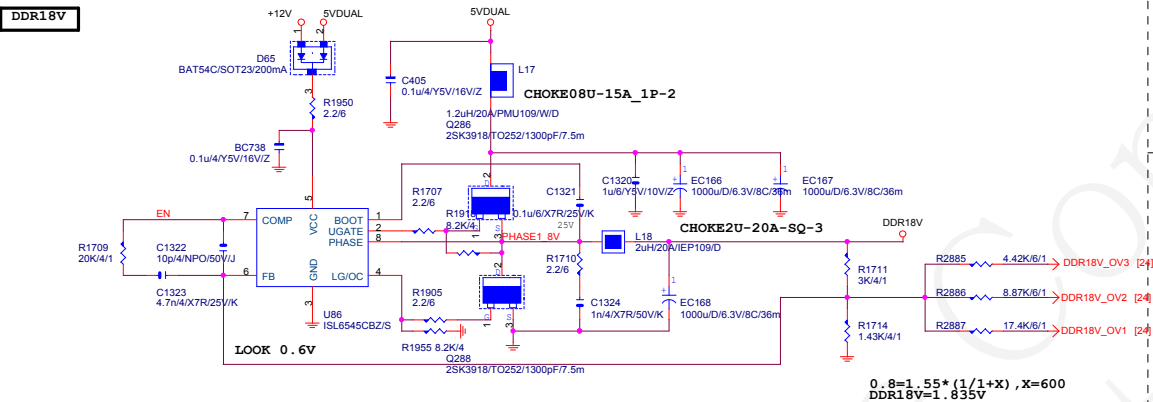
### 3VDUAL CIRCUIT



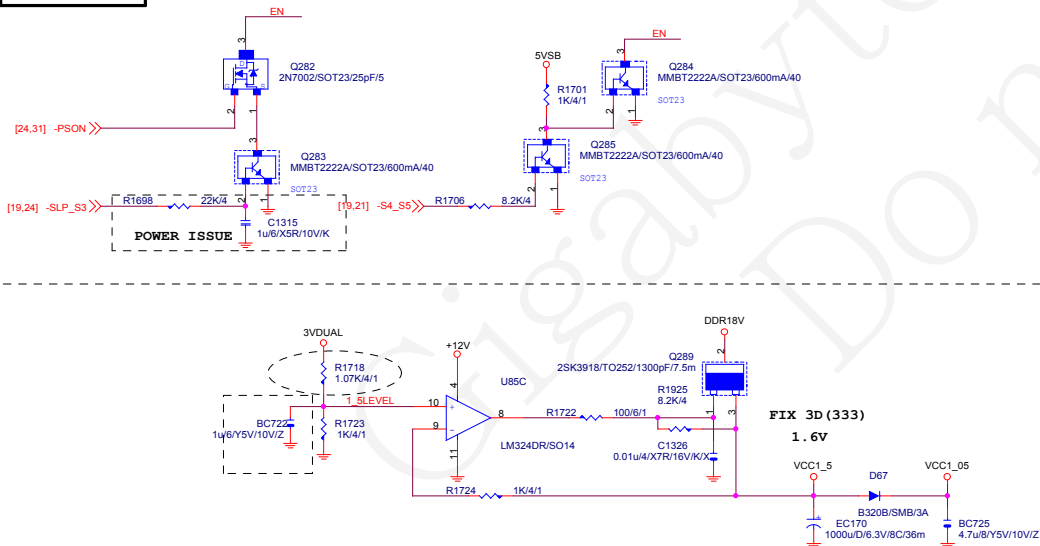
## DDRVTT



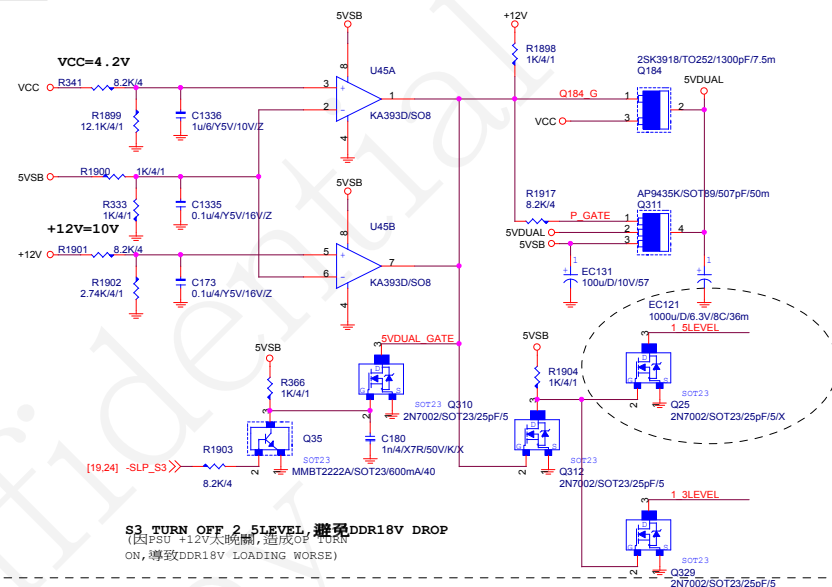
## DDR18V



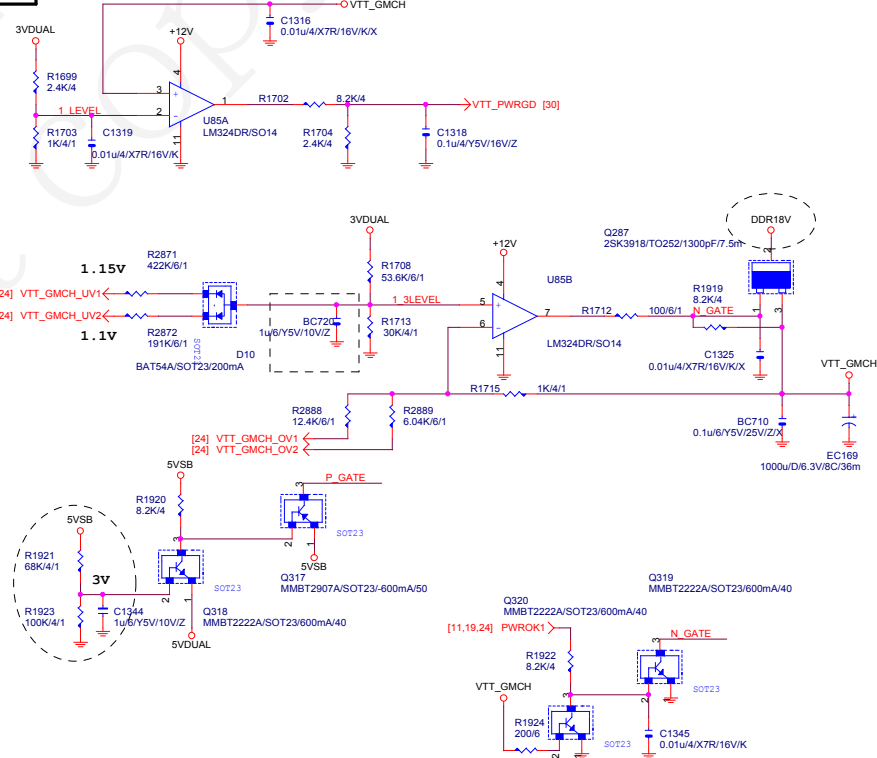
## POWER SEQUENCY



## 5VDUAL

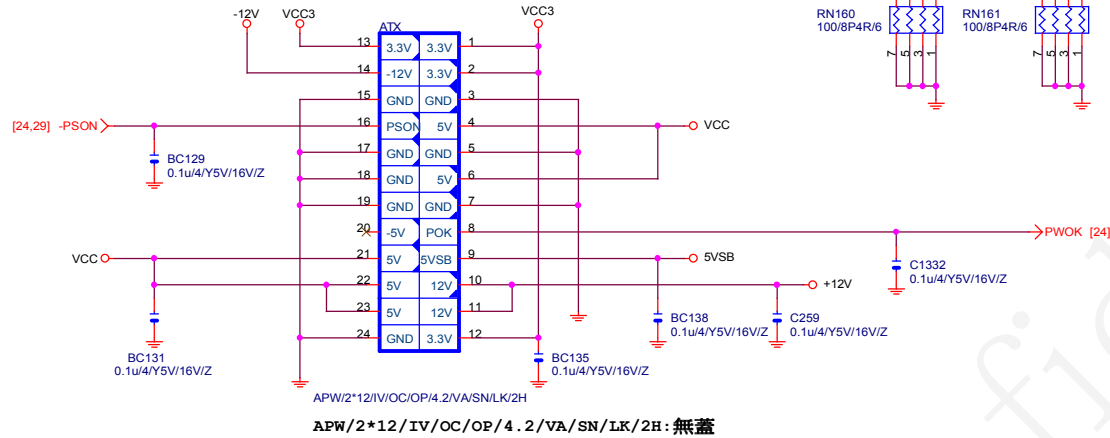


## POWER

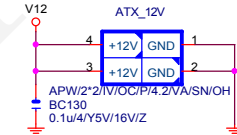




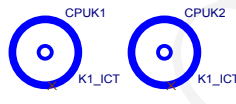
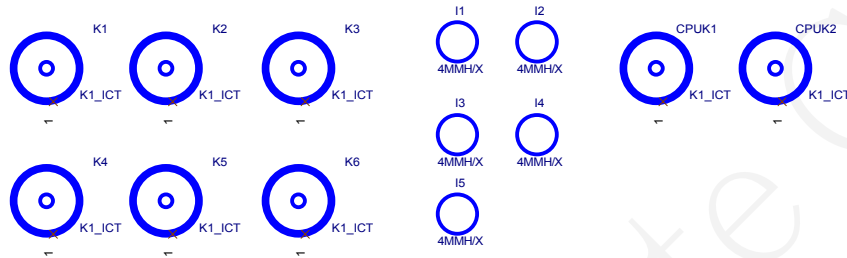
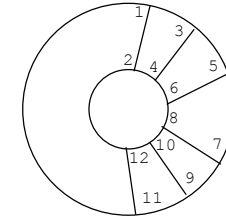
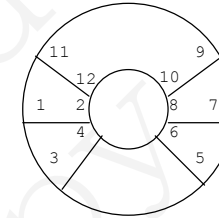
# ATXPWR\_24-2 ATX POWER CONNECTOR



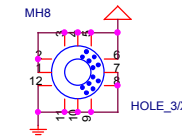
FIX PWR AcBel (ATX-400C-A2ADB)



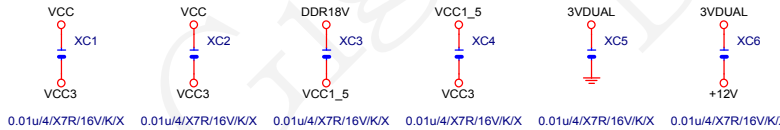
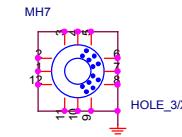
ATX\_4-1



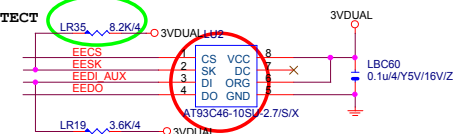
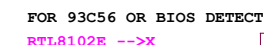
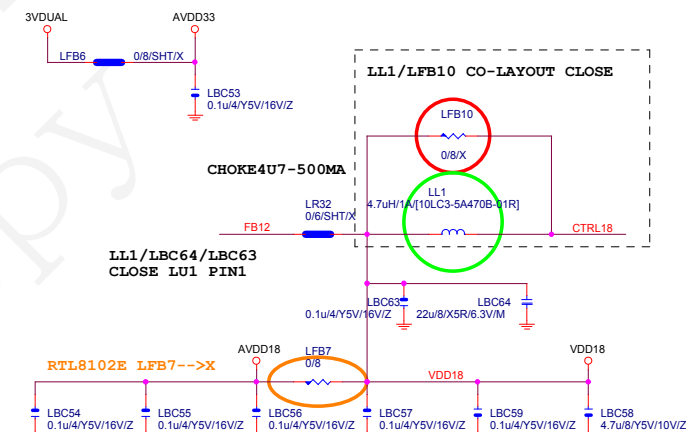
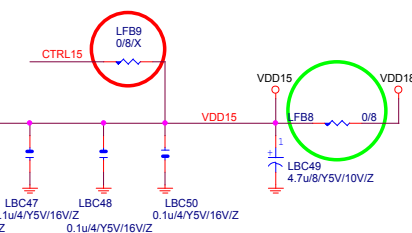
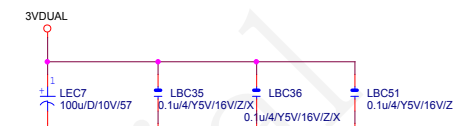
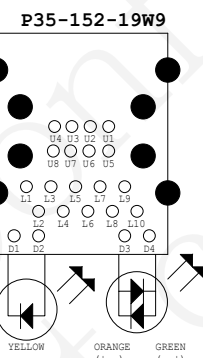
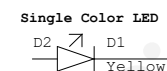
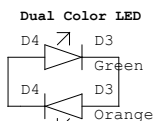
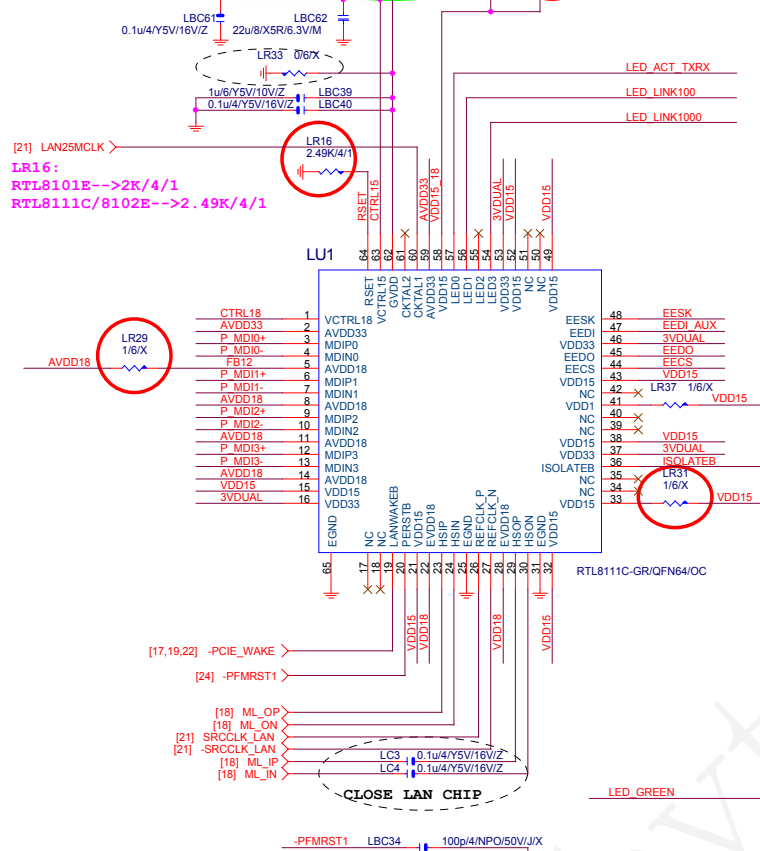
HOLE\_4-RH



HOLE\_4-RH-5MM



## PCIE-1G LAN

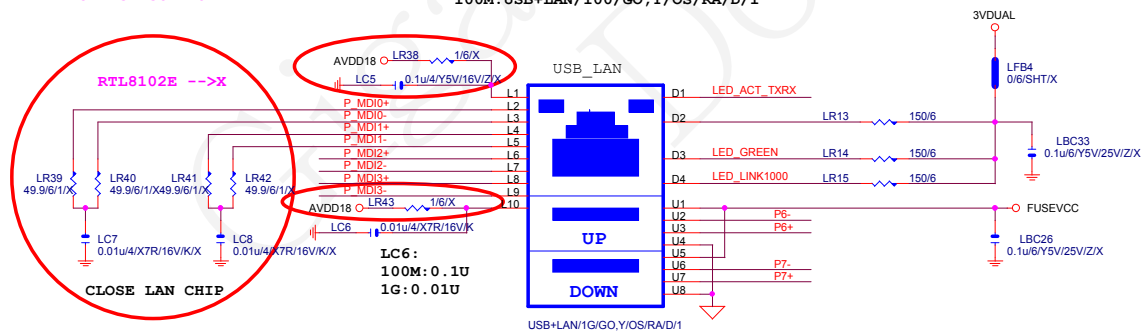


## USB LAN CONNECTOR

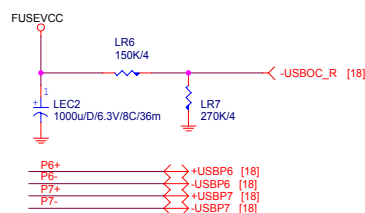
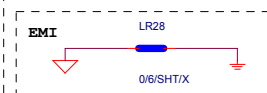
```
RTL8101E:LR38/LC5/LR43/LC6-->O
RTL8102E:LC5/LC6-->O
RTL8111C:LC6-->O
```

```
RTL8101E :L1+L10-->AVDD18+0.1U(BIOS DISABLE MDI-X FUNCTION)
```

```
1G   :USB+LAN/1G/GO,Y/OS/RA/D/1
100M:USB+LAN/100/GO,Y/OS/RA/D/1
```

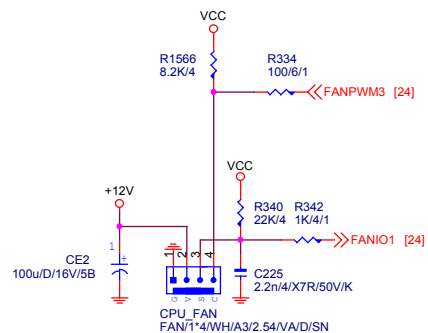


USB LAN

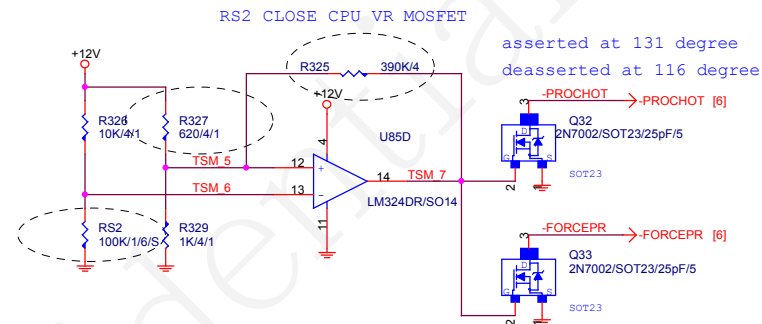




CPU SMART FAN	SMART FAN
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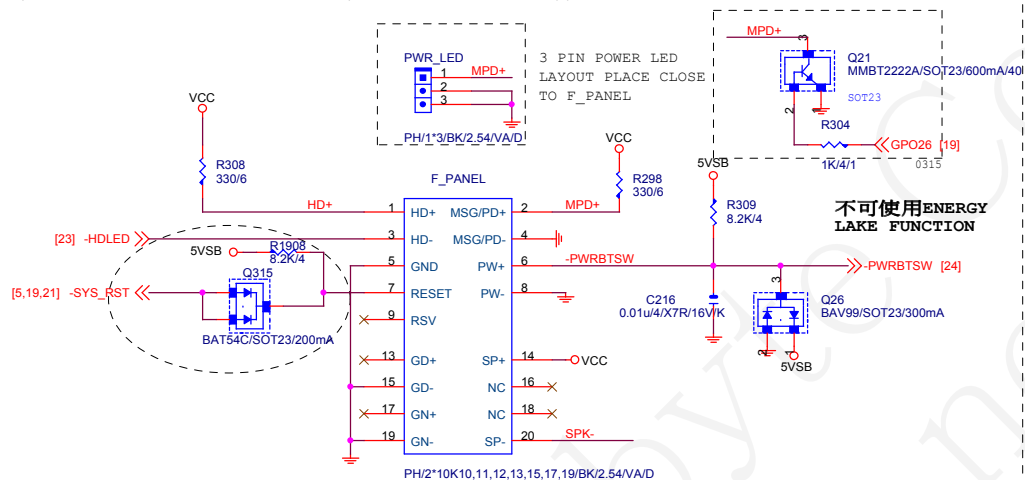


PROCESSOR HOT

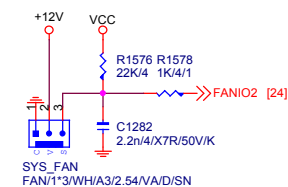


## INTEL FRONT PANEL

```
MPD- : (GPIO25--VCCSUS3+HI+HI+DEFINED(C3/C4/S/1/S3/S/4/S5))-->INTEL
```



## SYS FAN



**BUZZER**

